

### REMARKS

This paper responds to the Office Action dated August 26, 2005. Claims 1-41 and 45-54 are pending in this application.

#### §102 Rejection of the Claims

Claims 1-4, 8-14, 15-18 and 26-29 were rejected under 35 U.S.C. § 102(b) for anticipation by Cowles (U.S. 5,940,315). Applicant respectfully traverses the rejection. To anticipate a claim, the reference must teach every element of the claims. *M.P.E.P.* § 2131.

The Office Action admits that Cowles does not show “a number of memory cells,” a number of source lines,” or “a number of bit lines” (see Office Action pages 2 and 3). As such, the Office Action has not made a *prima facie* case of anticipation. Applicant requests withdrawal of the pending rejections.

#### *Concerning claims 1-4:*

Applicant cannot find in Cowles, among other things,

a strapping line of lower resistance than the wordlines coupled to a single continuous wordline in a single array wherein the strapping line bypasses only a portion in a middle region between a first and second end of the single continuous wordline, wherein the strapping line is spaced apart from adjacent conductive structures by a distance greater than a wordline pitch, and wherein the strapping line bypasses only a portion of a wordline within the single array and bypasses a different portion of a wordline within the single array than an adjacent strapping line,

as recited or incorporated in the contested claims. The conductive straps of Cowles apparently bypass a wordline across an entire memory array (see e.g., FIG. 2A). The Office Action asserts that the memory bank 100 of Cowles reads on the single memory array recited in claim 1 (see Office Action pg. 3). However, the memory bank 100 of Cowles is made of more than one memory array (see FIG. 2A), and Cowles states that the invention ... may be used with any number of arrays greater than one (see col. 3 lines 23-27). Thus, Cowles does not disclose the structure recited in claim 1. For example, Cowles does not contain any disclosure of “wherein the strapping line bypasses only a portion of a wordline within the single array,” or any

disclosure of “[wherein the strapping line] bypasses a different portion of a wordline within the single array than an adjacent strapping line,” as recited in claim 1.

*Concerning claims 8-14:*

Applicant cannot find in Cowles, among other things,

a number of strapping devices which bypass portions of the wordlines in the single array of parallel wordlines, wherein at least one portion of a single continuous wordline is only in a middle region between a first and second end of the single continuous wordline, and wherein adjacent strapping devices bypass only a portion of a wordline within the memory array and bypass different portions of adjacent wordlines within the memory array,

as recited or incorporated in the contested claims.

*Concerning claims 15-18:*

Applicant cannot find in Cowles, among other things,

a strapping line of lower resistance than the wordlines coupled to a single continuous wordline wherein the strapping line bypasses only a portion of the wordline within the memory array in a middle region between a first and second end of the single continuous wordline, wherein the strapping line is spaced apart from adjacent conductive structures by a distance greater than a wordline pitch, and wherein the strapping line bypasses a different portion of a wordline within the memory array than an adjacent strapping line,

as recited or incorporated in the contested claims.

*Concerning claims 26-29:*

Applicant cannot find in Cowles, among other things,

a strapping line of lower resistance than the wordlines coupled to a single continuous wordline wherein the strapping line bypasses only a portion in a middle region between a first and second end of the single continuous wordline and bypasses only a portion of the wordline within the memory array, wherein the strapping line is spaced apart from adjacent conductive structures by a distance greater than a wordline pitch, and wherein the strapping line bypasses a different portion of a wordline within the memory array than an adjacent strapping line,

as recited or incorporated in the contested claims.

Applicant respectfully requests reconsideration and allowance of claims 1-4, 8-14, 15-18, and 26-29.

Claims 5-7, 19-25, 30-36, 37-41, 45-48 and 49-54 were rejected under 35 U.S.C. § 102(b) for anticipation by Cowles. Applicant respectfully traverses the rejection.

*Concerning claims 5-7:*

Applicant cannot find in Cowles, among other things,

a plurality of separate strapping lines of lower resistance than the wordlines coupled to at least one of the number of wordlines in a single array wherein the strapping lines bypass a plurality of separate portions of a single continuous wordline, and wherein adjacent strapping lines bypass only a portion of a wordline within the memory array and bypass different portions of adjacent wordlines within the memory array,

as recited or incorporated in the contested claims. The conductive straps of Cowles apparently bypass a wordline across an entire memory array (see e.g., FIG. 2A). The memory bank 100 of Cowles is made of more than one memory array (see FIG. 2A), and Cowles states that the invention ... may be used with any number of arrays greater than one (see col. 3 lines 23-27). Thus, Cowles does not disclose the structure recited in claim 5. For example, Cowles does not contain any disclosure of “wherein adjacent strapping lines bypass only a portion of a wordline within the memory array”, or any disclosure of “[wherein adjacent strapping lines] bypass different portions of adjacent wordlines within the memory array,” as recited in claim 5.

*Concerning claims 19-25:*

Applicant cannot find in Cowles, among other things,

a number of separate strapping devices which bypass separate portions of a single continuous wordline in the single array of parallel wordlines, and wherein adjacent strapping devices bypass only a portion of a wordline within the memory array and bypass different portions of adjacent wordlines within the single array,

as presently recited or incorporated in the contested claims.

*Concerning claims 30-36:*

Applicant cannot find in Cowles, among other things,

a number of strapping devices which bypass only portions of the wordlines in the single array of parallel wordlines, wherein at least one portion of a single continuous wordline is in a middle region between a first and second end of the single continuous wordline, and wherein adjacent strapping devices bypass only a portion of the wordline within the memory array and bypass different portions of adjacent wordlines within the memory array,

as presently recited or incorporated in the contested claims.

*Concerning claims 37-41:*

Applicant cannot find in Cowles, among other things,

spacing a number of strapping devices over wordlines within a single memory array apart from adjacent strapping devices by a distance greater than a wordline pitch, wherein adjacent strapping devices bypass different portions of adjacent wordlines within the single memory array,

as presently recited or incorporated in the contested claims.

*Concerning claims 45-48:*

Applicant cannot find in Cowles, among other things,

attaching a strapping line of lower resistance than the wordlines to a single continuous wordline wherein the strapping line bypasses only a portion in a middle region between a first and second end of the single continuous wordline, wherein the strapping line is spaced apart from adjacent conductive structures by a spacing greater than a wordline pitch and wherein the strapping line bypasses only a portion of a wordline within the memory cell array and bypasses a different portion of a wordline within the memory cell array than an adjacent strapping line,

as presently recited or incorporated in the contested claims.

*Regarding claims 49-54:*

Applicant cannot find in Cowles, among other things,

attaching a number of strapping lines of lower resistance than the wordlines which bypass portions of the wordlines in the array of parallel wordlines, wherein at least one portion of a single continuous wordline is only in a middle region between a first and second end of the single continuous wordline, wherein the strapping lines are each located a distance from each other that is greater than the pitch, and wherein adjacent strapping lines bypass different portions of adjacent wordlines within the single array,

as presently recited or incorporated in the contested claims.

Applicant respectfully requests reconsideration and allowance of claims 5-7, 19-25, 30-36, 37-41, 45-48, and 49-54.

**CONCLUSION**

Applicant respectfully submits that the claims are in condition for allowance, and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney at (612) 349-9587 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

HUY T. VO

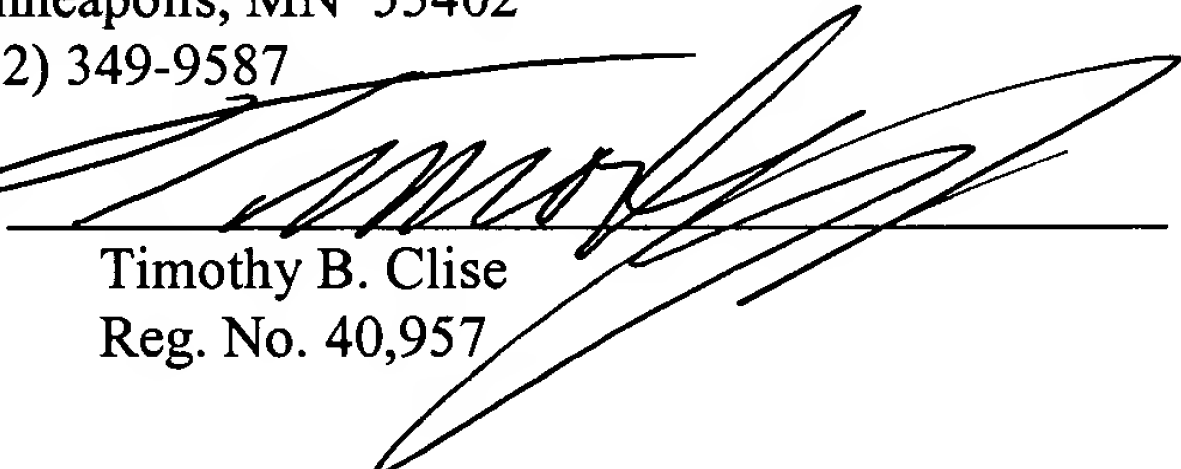
By his Representatives,

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Date

28 Nov '05  
(Monday)

By

  
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 28 day of November, 2005.

Name

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